### **A Survey of Formal Methods in Software Engineering**

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1

### **Structure of Talk**

3
7
8
9
10
16
17
23
29

### **1. An Example Formal Development 1.1. Fragments of A Domain Example**

1.

- 1. A net (graph) consists of sets of links (arcs) and hubs (nodes).
- 2. Links and hubs have unique identifiers.
- 3. The mereology of links identifies two unique hubs.
- 4. The mereology of hubs identifies a set of hubs.
- 5. From a set of links one can extract its link identifiers.
- 6. From a set of hubs one can extract its hub identifiers.
- 7. Mereology identifiers identify existing net parts.

- type 1. N = L-set  $\times$  H-set
- 2. LI, HI

### value

- 2. uid\_LI: L $\rightarrow$ LI, uid\_HI: H $\rightarrow$ HI
- 3. mereo\_L:  $L \rightarrow HI$ -set
- 4. mereo H:  $H \rightarrow LI$ -set
- 5.  $xtr_LIs: L-set \rightarrow LI-set$
- 6.  $xtr_HIs: H-set \rightarrow HI-set$

### axiom

- 7.  $\forall$  (ls,hs):N ·  $\forall l:L \cdot l \in ls \Rightarrow card mereo_L(l) = 2 \land$ 3. 7. mereo\_L(l)  $\subseteq$  xtr\_HIs(hs)  $\land$ 7.  $\forall$  h:H · h  $\in$  hs  $\Rightarrow$ 7.
  - $mereo_H(h) \subseteq xtr_LIs(ls)$

• The above models general nets, see left figure below.

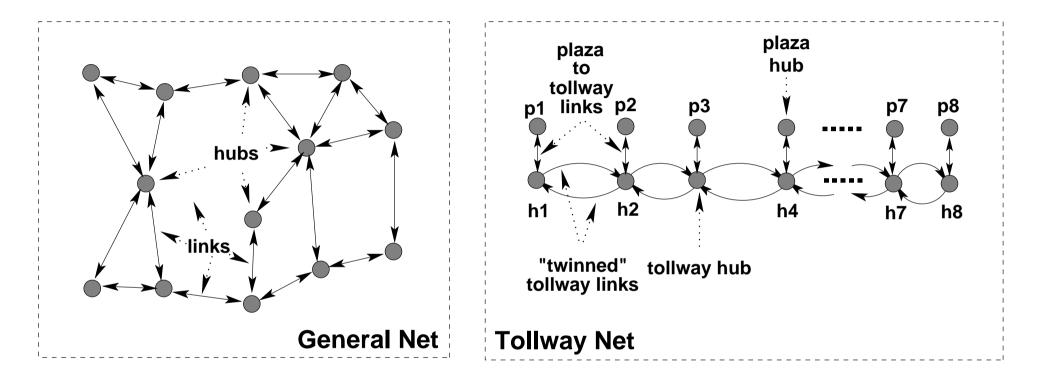


Figure 1: General Net and Toll-road Net

• Next we model toll-road nets, see right figure above.

### **1.2. Fragments of A Requirements Example** 1.2.1. Net Instantiation

- 8. A toll-road system consists of n toll-road segments and n+1 triples of toll plaza connections.
- 9. A toll-road segment is a pair of opposite traffic-direction toll roads.
- 10. A toll plaza connection consists of a toll plaza hub, a plaza-to-toll-road link and a toll-road hub.

### 1.2.2. Net Abstraction

Toll-road systems are concrete instantiations of nets.

- 11. We therefore define a net abstraction function value
- 12. which from toll-road systems

13. abstracts nets.

```
type
8. TRS = TRS<sup>*</sup> × TPC<sup>*</sup>
axiom
8. \forall (lll,hlh):TRS · len hlh = len lll + 1
type
9 TRS = L \times L
10. TPC = H \times L \times H
```

- 11. abs N: TRS  $\rightarrow$  N
- 12.  $abs_N(trsl,tpcl) \equiv$
- $({ \{lf, lt\} | (lf, lt): TRS \cdot (lf, lt) \in elems trsl }$ 13.
- 13.  $\cup$  {l|l:L·(\_,l,\_)  $\in$  elems tpcl},
- {{hp,ht}} $(hp, ht): TPC (hp, ht) \in elems tpcl})$ 13.

### **1.3. Fragments of A Software Design Example**

- We decide to implement the toll-road net
- as a collection of relational database relations.
- 14. The transport net relational database 17. For a given link there is one consists of five relations. mereology tuple.
  - a relation for hub mereologies,
  - a relation for hub attributes,
  - a relation for link mereologies,
  - a relation for link locations, and
  - a relation for other link attributes.
- 15. For a given hub (hi:HI) there is any set of mereology tuples.
- 16. For a given hub there is one other attributes tuple.

- - 18. For a given link there is a set of at least two location tuples (ll:LL).
  - 19. For a given link there is one other attributes tuple.

### type

- 14. RN = HM-set  $\times HA$ -set  $\times LM$ -set  $\times LL$ -set  $\times LA$ -set
- 15.  $HM = HI \times LI$
- 16.  $HA = HI \times LOC \times ...$
- 17.  $LM = LI \times HI \times HI$
- 18.  $LL = LI \times LOC$
- 19.  $LA = LI \times LEN \times ...$

# 2. What is Software?

- **Software.** By software we shall understand all the following kinds of documents:
  - « Planning Docs.
    - Background
    - Motivation
    - © Teams
    - © Etcetera.
  - « Development Docs.
    - Domain description
    - $\infty$  Requirements prescription
    - $\infty$ Software design & code
    - $\infty$  Test data and results

- Model checking
- Proof of properties
- Manuals
  - © Installation

  - Maintenance
  - ∞ etcetera
- « Project Docs.
  - Planning, Budget, AccountsProject Logs

### 3. What is a Method?

• Method. By a method we shall understand

- « a set of **principles**
- $\circledast$  for  $selecting \ {\rm and} \ applying$
- $\otimes$  a number of techniques and tools
- « in order to **analyse** and **synthesize** (construct) an **artifact**.

- Example **tools**: specification and coding languages, theorem provers, model checkers, test tools, etc.
- Example **techniques**: abstract and concretisation, proof techniques, etc., refinement, etc.
- Example **analyses**: consistency, completenes, invariants, etc.

### 4. What is a Formal Method?

• Formal Method. By a formal method we shall understand

- $\otimes$  a **comprehensive** set of method techniques and tools
- ∞ which have a **formal foundation in mathematics**,

 $\otimes$  that is:

- each specification language has
  \* a mathematical syntax,
  \* a mathematical semantics, and
  \* a proof system;
  while supporting
  - \* refinement, \* model checking, \* proof, \* test,

etcetera, tools obey these formalisms.

### 5. History of Formal Method Specification Languages

• A selection of basically model-oriented methods:

• VDM	11
• Z	12
• RAISE	13
• B, Event B	14
• Alloy	15

# • Other formal methods are property-oriented:

- **⊗ Maude,** etc.

### 5.1. **VDM**

- $\bullet$  VDM: [IBM] Vienna [laboratory software] Development Method 1973 1975
- PL/I Compiler Devt. P. Lucas, H. Bekič (†), C.B.Jones and D.Bjørner
- Springer LNCS 61 1978 and Prentice-Hall 1982



- Dansk Datamatik Centre: CHILL (CCITT) and Ada (US DoD) Language Definitions and Compiler Devts 1981–1984. DDCI Inc., USA
- VDM SL (Spec.Lang.) Standard, 1996: ISO/IEC 13817/1
- VDM Toools: JFITS, CSK, Japan: http://www.vdmbook.com/download.html
- http://www.**vdmportal.org**/twiki/bin/view
- Lively VDM activity in Japan and Europe: Research and Industry

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### 5.2. Z

- $\bullet$  Z for Zermelo (18711953) Fraenkel (1891–1965) Set Theory
- Z is developed by **Jean-Raymond Abrial** between 1980–1990.
- Lively research around Z in mostly England (Woodcock, Univ. of York)
- Major british industrial uses of Z:

« etcetera ...

- http://formalmethods.wikia.com/wiki/Z\_User\_Group
- **Z Standard** ISO/IEC 13568, 2002

### 5.3. **RAISE**

- RAISE: **R**igorous **A**pproach to **I**ndustrial **S**oftware **E**ngineering
- Result of an EU ESPRIT BRA project with DDC: Dansk Datamatik Center (**Bjørner:** Instigator) and STL: Standard Telephone Labs., UK, etc.
   1985–1990
- RAISE is being used at Terma Space Division, a Danish Systems house.
- RSL (RAISE Spec.Lang.) captures concurrency and features Duration Calculus
- $\bullet$  RAISE was the formal method being used at UNU-IIST, Macau, 1992–2009
  - $\circledast$  Chinese Railways
  - $\circledast$  Vietnam Ministry of Finance

 $\circledast$  Philippine Min. of Telecomm.

 $\circledast$  Chennai Harbour Management, India



• Primarily designed by **Søren Prehn** and **Chris George**.

I am using it !

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### 5.4. **B**, **Event B**

- B for **Bourbaki:** Collective pseudonym author name of mathematics monographs: http://www.en.wikipedia.org/wiki/Nicolas\_Bourbaki
- B was developed by Jean-Raymond Abrial between 1990--2000.
- Event-B is developed by Jean-Raymond Abrial since 2000.
- Event-B evolved from a rather total redesign of B.
- Event-B captures a form of concurrency.
- http://www.event-b.org/
- French B and Event-B industrial users.
- Academic base in France (Nancy) and the UK (Southhampton)





- Masterminded by **Daniel Jackson**
- An elegant VDM "derivative"
- http://alloy.mit.edu/alloy/
- Great for teaching abstraction and formal methods.
- My strongest recommendation for introduction for formal methods.

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# 6. The Triptych Software Development Model 6.1. The Dogma

- Before software can be designed (i.e., coded, programmed)
- one must a a reasonable understanding of its requirements.
- Before requirements can be prescribed
- one must a a reasonable understanding of their domain.

# 6.2. Consequences of the Dogma

$$\mathcal{D}, \mathcal{S} \models \mathcal{R}$$

### 7. Formal Methods: State-of-Affairs 7.1. History

• First industry scale formal developments were the DDC CHILL and Ada compiler developments: **1980–1984** 

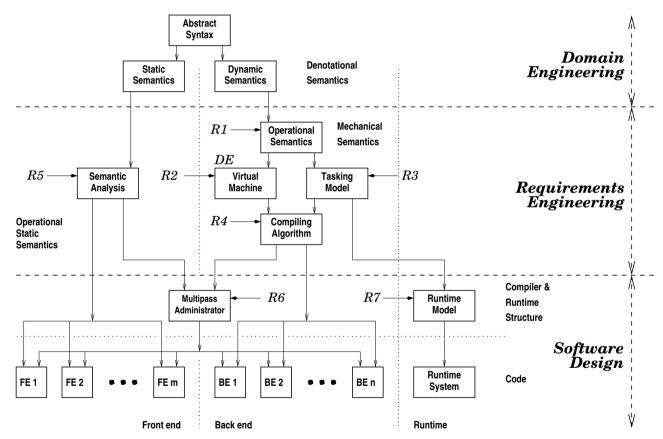


Figure 2: CHILL and Ada Software Development Graphs

17

### 7.2. Industrial Uptake

• Slow, but steady

### 7.2.1. Software Industries

- CSK • Japan: • Denmark: Terma • England: Altran-Praxis • Netherlands: CHESS • France: ClearSy • Sweden: Telelogic (IBM) ISP/RAS • Germany: Verified Sys. • Russia: Ansaldo SRI, Microsoft • Italy: • USA: 7.2.2. Hardware Industries: Verified Chip Designs • Intel
- $\bullet AMD$

- Cadence Berkeley
- IBM

### 7.2.3. More FM URLs

- ERCIM FMICS: Europ.Res.Cons. Industrial Critical Systems DEPLOY Success Stories
   http://www.fm4industry.org/index.php/DEPLOY\_Success\_Stories
- US DoD NASA: Langley Formal Methods <a href="http://shemesh.larc.nasa.gov/fm/">http://shemesh.larc.nasa.gov/fm/</a>
- SRI Inc., Computer Systems Lab.
   http://www.csl.sri.com/programs/formalmethods/
- Laboratory for Reliable Software (LaRS) http://lars-lab.jpl.nasa.gov/
- Altran-Praxis: Formal Computing
   http://www.altran-praxis.com/formalComputing.aspx
- ClearSy B Method
   http://www.clearsy.com/our-specific-know-how/b-method/?lang=en
- Formal Methods Wiki
   http://formalmethods.wikia.com/wiki/Formal\_Methods\_Wiki

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### 7.3. Industrial Needs

- Industries that are using FMs on projects
  - need all SEs on that project to have learned one or another of the methods listed earlier;

  - $\otimes$  these software houses need a steady local supply of such professionally trained SEs.

# 7.4. University Courses 7.4.1. BSc Courses

or

- Functional Programming
- Imperative Programming
- Logic Programming
- Parallel Programming
- Abstraction and Modelling

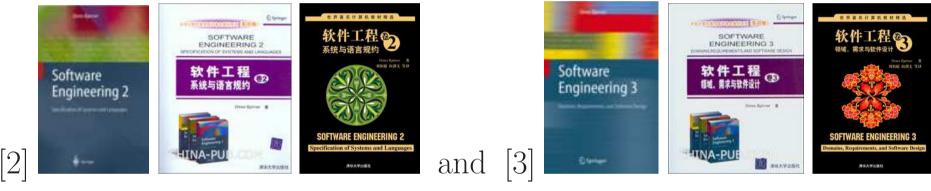


Standard ML Spec# Prolog CSP (as in e.g. Java) See [1]



### 7.4.2. MSc Courses





• Advanced Software Verification: Formal Testing, Model Checking, Theorem Proofs

# 8. Formal Methods: Some Observations 8.1. Formal Methods and Formal Techniques

- By **formal methods** software development we mean
  - a development which uses formal specification languages
     in all there phases of development: domains, requirements and design
- By **formal techniques** software development we mean
  - $\otimes$  a development which uses one or another formal techniques  $\otimes$  usually design only —
  - $\otimes$  these formal techniques could be
    - static analysis,formal testing,

model checking,theorem proving.

### 8.2. From Mono-language to Multi-language Specification

∞ CSP,	∞ Petri Nets,	∞ Temporal Logic,
∞ MSC,	∞ State Charts,	∞ etcetera,

• CSP and DC (Duration Calculus) can be used with RSL.

### 8.3. Sociology of Acceptance of Formal Methods 8.3.1. Industry

• The software (SW) industry has been moderately successful  $\otimes COTS^1$  SW in partocular (MS, etc.),

 $\otimes$  but Turn-key SW projects have failed on a gigantic scale,

 $\otimes$  yet the SW industry persists in believing

- $\otimes$  that such projects can be staffed by non-professionals.
- The SW industry, in general, resists FMs

 $\otimes$  claiming that there are no statistics supporting FMs:

 $\otimes$  there are such "statistics",

 $\otimes$  but real such requires at least a triplet of 1000 test devts.

• And: what would they do with all their non-professional SEs?

<sup>&</sup>lt;sup>1</sup>COTS: Commercial off-the-shelf

### 8.3.2. Universities

- $\bullet$  In a mathematics dept. all mathematicians
  - $\otimes$  know enough of colleagues' specialised field,
  - $\otimes$  to appreciate it, and "interface" to, i.e., make use of it.
- In most computer science depts. such is not the case:

   So-called theoretical CSs do not know how to develop software,
   So-called the kind of FMs covered in this talk.
- Their students, consequently, do not take FMs serious.

### 8.4. Inevitability of FMs

- The MS Distributed File System Replication DFS R "Story"<sup>2</sup>

   Microsoft is increasingly committed to Formal Techniques
- If software can have guaranteed warranties ('correctness'),
  & then that will occur
  & and software development will hence use FMs.

<sup>&</sup>lt;sup>2</sup>http://research.microsoft.com/pubs/70451/tr-2007-75.pdf

### 8.5. Textbooks

- VDM: J. Fitzgerald and P. G. Larsen. *Modelling Systems Practical Tools and Techniques in Software Development*. Cambridge University Press, The Edinburgh Building, Cambridge CB2 2RU, UK, 1998.
- Z: J. C. P. Woodcock and J. Davies. Using Z: Specification, Proof and Refinement. Prentice Hall International Series in Computer Science, 1996.
- RAISE: D. Bjørner. Software Engineering, Vol.1: Abstraction and Modelling, Vol.2: Specification of Systems and Languages, Vol.3: Domains, Requirements and Software Design. Texts in Theoretical Computer Science, the EATCS Series. Springer, 2006.
- **B**, **Event B:** J.-R. Abrial.
  - The B Book: Assigning Programs to Meanings and Modeling in Event-B: System and Software Engineering. Cambridge University Press, Cambridge, England, 1996 and 2009
- Alloy: D. Jackson. Software Abstractions: Logic, Language, and Analysis. The MIT Press, Cambridge, Mass., USA, April 2006. ISBN 0-262-10114-9.

# 9. Closing

- This has been a "lightweight" survey of formal methods and industry.
- $\bullet$  This was deliberately so —

 $\otimes$  so that you can ask questions

- $\otimes$  and I can hopefully answer them;
- $\otimes$  at least we can discuss the state-of-affairs.

### Many Thanks — and: Questions?